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EFFECT OF CRYSTALLOGRAPHIC ORIENTATION AND EMPLOYMENT OF DIFFERENT CUTTING TOOLS ON MICRO-END-MILLING OF MONOCRYSTALLINE SILICON

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ABSTRACT

This paper presents the research on the effect of crystallographic orientation and different cutting tool effect during micro-milling of (001) silicon wafer. Excessive generation of undesirable surface and subsurface damages often occurs when machined at thick depth of cut of several hundreds of microns. Up-milling operations along <100> and <110> directions were performed on a (001) wafer and the results shows that machining surfaces along <100> were of better quality than those of <110> and is in agreement with previous studies. In addition, comparative studies of diamond coated, CVD and single crystal diamond end-mills were performed along [0̅1̅0] at 150 µm deep. Results shows that diamond coated tool generates the least edge chipping amongst the three. This might be due to the large negative rake angle creating highly compressive hydrostatic pressure in the cutting zone and therefore suppressing the crack propagation. Furthermore, no visible defects were detected on the bottom machined surface when machined by CVD and single crystal diamond end-mills. Surface edge chipping however remains a challenge even though micro-milling were performed along <100> with single crystal diamond end-mill. Apart of milling along <100>, protection to the top silicon surface are required to achieve fracture free quality micro-milled silicon.
Keywords: Micro-End-Milling, Monocrystalline Silicon, Crystallographic Orientation, Diamond End-Mills.

1 INTRODUCTION
Hard brittle materials such as silicon, silicon carbide and gallium nitride are ubiquitously used in the semiconductor and Micro-Electro-Mechanical Systems (MEMS) industrial sectors. Fabrication of these materials micro-scale level can be challenging when stringent specifications such as a good surface finish and high accuracy form factor are required. Additive techniques are not generally applicable for crystalline materials due to the difficulty of patterning the growth of good quality material. As such, subtractive techniques such as deep reactive ion etching (DRIE) and either isotropic or anisotropic Wet Etching were typically used in the MEMS/semiconductor industries. These techniques can achieve patterned subtraction by patterning etch stops via photolithography. These techniques lend themselves well to large scale manufacture, but require lithographic processing and are largely limited to flat surfaces.¹,²

Furthermore, laser based lithographic techniques have been shown to achieve good quality finishing. However it requires picoseconds lasing which is very slow and is less suitable for processing of transparent materials such as Gallium Nitride. Alternatively, silicon is also manufactured mechanically by grinding, lapping and polishing. Thus, mechanical micro-milling is a potential maskless alternative that could achieve new types of non-planar, potentially three-dimensional structures. Machining of crystalline materials with ductile mode cutting to achieve good surface integrity had been studied with different approaches using ultra-precision micromachining.³,⁴,⁵,⁶ In addition, Huo et al. had also systematically characterized the surface and subsurface in micro milling of monocrystalline silicon.⁷

Despite micro-milling showing potential in micromachining of ductile materials such as aluminum, excessive generation of surface and subsurface damages remains a challenge in
silicon micro-milling. This often occurs when silicon is machined at hundreds of microns in depth. Due to a lack in studies on the effect of crystallographic orientation and behaviour of different cutting tools on silicon micro-milling, this paper aims to provide fundamental understandings on the anisotropic behaviour and the effect of different cutting tools on silicon during micro-milling.

1.1 Representation of Silicon Crystallographic Orientation using Miller Index

Silicon has a cubic crystalline structure in which its crystal lattice can be explained using a simple cube. Miller index, denoted by three integers \( hkl \) which corresponds to the respective XYZ axes are used to represent the designated planes and directions with respect to the principal axes.\(^2\) Specific direction and planes are represented by square and round brackets respective. In the case of an opposite direction or plane, a bar is used to denote the opposite of a specific direction or plane rather than a minus sign.\(^8\)

Furthermore, the integers which denotes a specific plane is similar to the specific direction perpendicular to that plane. For example, the (100) plane represents the plane on the X axis. The direction of X axis represented by [100], is perpendicular to the X axis plane. Figure 1 shows the cubic structure of silicon crystal with its principal axes denoted using Miller Indices. In addition, Table 1 also shows the definitions of each representation to the descriptor of either direction or plane.

![Figure 1: Miller Index representation on a cubic system.](image)

**Table 1**: Definitions of Miller Index notation for plane and direction.
<table>
<thead>
<tr>
<th>Denotations</th>
<th>Definitions</th>
</tr>
</thead>
<tbody>
<tr>
<td>{ }</td>
<td>Family of planes. E.g. {100} = (100), (010), (001) and reciprocals.</td>
</tr>
<tr>
<td>( )</td>
<td>A specific plane.</td>
</tr>
<tr>
<td>&lt;&gt;</td>
<td>Family of directions. E.g. &lt;100&gt; = [100], [010], [001] and reciprocals.</td>
</tr>
<tr>
<td>[ ]</td>
<td>A specific direction.</td>
</tr>
</tbody>
</table>

### 1.2 Anisotropy Properties of Silicon

Location of the primary flat of a (001) silicon wafer is aligned with the [110] direction with its natural cleavage plane usually runs along <110> and <111>. Fracture on a silicon is orientation-dependence due to the different bonding forces between each atoms at different directions within the silicon lattice. Mechanical properties such as the Young’s modulus and fracture toughness were also studied to explain the anisotropic fracture behaviour of silicon during machining. Young’s modulus of the silicon can be calculated using Equation (1):\(^9\)

\[
\frac{1}{E_{hkil}} = S_{11} - 2\left(\frac{1}{2}S_{12}\right) - \frac{1}{2}S_{44}(m^2n^2+p^2+m^2p^2)
\]

(1)

Where:

- \(E\) is the Young’s modulus
- \(hkil\) is the three-integer triples corresponding to XYZ axis in a coordinate system.
- \(m, n\) and \(p\) are the cosine of angles between direction of interest denoted by \([hkil]\)
- \(S_{ij}\) is the compliance constant \(S_{11} = 7.68; S_{12} = -2.14; S_{44} = 12.6\)

According to Equation (1), Young’s modulus is similar on all crystallographic directions for a (111) wafer plane at 188 GPa. However the Young’s modulus on a (001) wafer plane exhibits a fourfold symmetry behaviour at 169 GPa and 130 GPa along the <110> and <100> directions respectively. Fracture toughness of silicon, which determines its resistance against crack propagation, is also highly orientation-dependence. Using Griffith criterion, fracture toughness at each crystallographic directions can be computed using Equation (2):\(^10\)

\[
K_{IC} = \sqrt{\frac{2\gamma_{hkil}E_{hkil}}{(1-v^2)}}
\]

(2)
Where:

- \( \gamma_{hkl} \) is the surface energy of any \( hkl \) directions.

Values of the fracture toughness reported by several articles for {100}, {110} and {111} wafers are shown in Table 2. According to the behaviour of these fracture toughness as shown in Table 2, it can be recognized that <100> directions has the highest resistance against crack propagations when compared to <110> and <111>.

### Table 2: Silicon’s fracture toughness values from literatures.

<table>
<thead>
<tr>
<th>Fracture Planes</th>
<th>Fracture Toughness (MPa.m(^{1/2}))</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>{100}</td>
</tr>
<tr>
<td>(Jaccodine et al.)(^{11})</td>
<td>0.75</td>
</tr>
<tr>
<td>(Chen et al.)(^{12})</td>
<td>0.95</td>
</tr>
<tr>
<td>(Hayashi et al.)(^{13})</td>
<td>1.14 – 1.19</td>
</tr>
<tr>
<td>(Tanaka et al.)(^{14})</td>
<td>0.89</td>
</tr>
</tbody>
</table>

### 1.3 Review on the Effect of Crystallographic Orientation on Silicon Micromachining

During the early discovery of surface anisotropy on silicon mechanical micromachining, Blackley et al. shows that the damages caused by orientation-dependent was contributed by the variation of resolved tensile stress in the \{111\} slip planes.\(^{2}\) In addition, relative changes between the machined orientation plane and cutting direction during micro-turning of silicon was also consider in the recent years to explain the machining quality of orientation-dependent silicon surface.

Shibata et al. performed nanoscale ductile mode micro-turning on silicon with crystal surface of (001) and (111).\(^{15}\) A slip model was used in the qualitative analysis to explain the machined surface effect when turned at different directions. The experiment also shows the anisotropic effect of machined surface taking the form of a fourfold and threefold symmetric across the center of (001) and (111) wafer respectively. Hung et al. studied the effect of crystallographic orientation during ductile mode turning of (100) silicon wafer.\(^{16}\) The study shows that micro pitting occurs along <100> while ductile mode was achieved along <110>.
Cheung had also performed ductile mode micro-turning on (001) and (110) wafers.\textsuperscript{17} Surface roughness measured across each crystallographic directions indicates a variation in roughness value when the relative machined surface plane and the cutting directions changes during turning. O’Connor et al. shows the measurement of critical chip thickness as a function of crystallographic orientation under different cutting conditions and directions on a (001) wafer.\textsuperscript{18} Results from the experiments indicates that cutting force measured from dynamometer varies when turned at different directions and ductile mode cutting on (001) wafer can be achieved at a maximum depth of cut of 120 nm in [100] and 40 nm in [110] respectively. Jasinevicius et al. had also showed that the critical chip thickness in ductile material removal mode for (100) silicon wafer reaches a maximum of 285 nm in [100] and 110 nm in [110] respectively.\textsuperscript{19} Cracks were also found to be easily formed along <110> when micro-turned at sub-micrometer depth of cut.

1.4 Review on the Effects of Different Cutting Tools on Silicon Micro-Milling
Coated Tungsten carbide (WC) and single crystal diamond tools are commonly used for micromachining of silicon. Rusnaldy et al. had performed a comparative study on micro-milling with TiCN, TiAlN and diamond coated tungsten carbide tools on a (111) silicon wafer.\textsuperscript{20} Experiments were conducted in terms of ductile regime machining with an axial depth of cut at 0.3 and 0.5 µm. Results from the investigation shows that micro-milling performed with diamond coated tungsten carbide tool produces the least amount of fracture along the machined surface with a surface roughness measured at 60.7 µm.

Although coating helps to increase resistance against wear and resistance, the thickness of the coatings would increase in cutting edge radius and therefore causing the cutting tool to be blunt. Due to the size effect in microstructure affecting the machining quality of the work piece, coating on micro-cutting tools might have a negative effect to the micromachining performance. Chemical vapour deposited and single crystal diamond tools possessing
exceptional hardness have been introduced for micromachining during the recent years.\textsuperscript{21} As the cutting edge radius is in sub-micrometer, the tool cutting edge is considered to be very sharp for micro-cutting processes.
2 EXPERIMENTAL METHODOLOGY

2.1 Methods for Effect of Crystallographic Orientation on Silicon Micro-Milling

2.1.1 Workpiece and Machining Equipment Setup
A 15 x 15 mm work-piece was cleaved from a (001) silicon with 500um and 100 mm in thickness and diameter respectively as shown in Figure 2. Micro-milling operations were conducted along <100> and <110> on the (001) surface. Each cutting direction was varied at 45° between 0° and 360° and the machining conditions were shown in Table 3.

The sample was bonded onto a ground metal plate and clamped onto a work-piece holder. Experiment was conducted on Nanowave MTS5R micro-milling system as shown in Figure 3. It consists of a three-axis motion stage, whereby the Y axis actuates the high speed air bearing spindle with a 0.5 mm diameter UT coated tungsten carbide end mill by Air Bearings Ltd and Union Tool Co. respectively. Furthermore, the X and Z axes actuate the work-piece holder and Kistler dynamometer for cutting force measurement during machining.

![Figure 2: Illustration of silicon work-piece and machining schematic.](image1.png)

![Figure 3: Nanowave MTS5R micro-milling system for performing milling along <100> and <110> directions on a (001) silicon wafer.](image2.png)
Table 3: Machining conditions for micro-milling operations on a (001) silicon wafer.

<table>
<thead>
<tr>
<th>Cutting Conditions</th>
<th>Parameters</th>
</tr>
</thead>
<tbody>
<tr>
<td>Spindle Speed (rpm)</td>
<td>40,000</td>
</tr>
<tr>
<td>Cutting Speed (m/min)</td>
<td>62.83</td>
</tr>
<tr>
<td>Feed Per Tooth (μm/tooth)</td>
<td>0.15</td>
</tr>
<tr>
<td>Depth of Cut (μm)</td>
<td>10</td>
</tr>
</tbody>
</table>

2.1.2 Cutting Strategy – Up Milling

Micro-milling mechanics includes two cutting operations namely slot and side milling. Furthermore, there are two strategies within side milling comprising of either up or down milling. Chip formation in ductile mode occurs when the uncut chip thickness is sufficiently small with the ratio of tool cutting edge radius to uncut chip thickness > 1.

In an up-milling process as shown in Figure 4, the uncut chip thickness begins from a minimum value at the start of cut and approaches a higher value when the cutting tool rotates. This allows minimal material removal at the start of each cut while its critical chip thickness will only be reached at the later stage. Due to the cutter rotating almost instantaneously along the respective cutting directions, the final machined surface is therefore formed by the exposed surfaces from removed materials in the ductile region during multiple cutting passes. As silicon is brittle, up-milling was applied to ensure minimal damages will be incurred on the final machined surfaces which is aligned with the cutter’s feed direction.

![Figure 4: Schematic of up-milling process where the chip thickness begins at minimal and increases towards the end of each cut.](image)
2.2 Effect of Different Cutting Tools on Silicon Micro-Milling

Comparative studies on the effect of different cutting tools on silicon micro-milling were conducted using three different end mills namely diamond coated tungsten carbide tool, chemical vapour deposited (CVD) diamond tool and single crystal diamond tool. Diameter of each cutting tool were similar at Φ0.5 mm. Figure 5 shows a schematic of the machining plan on the (001) wafer. Table 4 also shows the parameters and profile specifications for machining of each wafer and the conditions were kept constant throughout each milling operations.

Figure 5: Schematic of machining plan for investigation on the effect of different cutting tools on silicon micro-milling.

Table 4: Machining conditions for comparative studies on different cutting tools.

<table>
<thead>
<tr>
<th>Machining Parameters</th>
<th>Parameters</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cutting Conditions</td>
<td></td>
</tr>
<tr>
<td>Spindle Speed (rpm)</td>
<td>40,000</td>
</tr>
<tr>
<td>Cutting Speed (m/min)</td>
<td>62.83</td>
</tr>
<tr>
<td>Feed Per Tooth for Diamond Coated Tungsten Carbide Tool (μm/tooth)</td>
<td>0.075</td>
</tr>
<tr>
<td>Feed Per Tooth for CVD and Single Crystal Diamond Tool (μm/tooth)</td>
<td>0.15</td>
</tr>
<tr>
<td>Axial Depth of Cut (μm)</td>
<td>10</td>
</tr>
<tr>
<td>Width of Cut (μm)</td>
<td>500</td>
</tr>
<tr>
<td>Machine Profile Specifications</td>
<td></td>
</tr>
<tr>
<td>Total Depth of Cut (μm)</td>
<td>150</td>
</tr>
<tr>
<td>Slot Length (mm)</td>
<td>6</td>
</tr>
<tr>
<td>Cutting Directions</td>
<td>[010]</td>
</tr>
</tbody>
</table>
3 RESULTS AND DISCUSSION
Quantitative and qualitative analysis were conducted on the cutting performance of silicon after micro-milling operations. A distinct crystallographic orientation dependence behaviour was observed. In this section, metrology techniques and the experimental results will be discussed.

3.1 Effect of Crystallographic Orientation during Silicon Micro-Milling

3.1.1 Cutting Force Analysis
Drift compensation was performed by zeroing all the measured data prior to signal processing. Raw cutting force data were then processed with a 2nd order low pass filter to remove any high frequency noises. Ten cycles of cutting force at each cutting directions were obtained for analysis. Finally, the average resultant cutting force at each direction was computed by Equation (3):

$$\text{Average Resultant Cutting Force} = \sqrt{\frac{F_x^2 + F_y^2 + F_z^2}{n}}$$

(3)

Where:

- $F_x$, $F_y$ and $F_z$ are the processed cutting force at each time point at a specific cutting direction [$hkl$].
- $n$ is the total time point at a specific cutting direction [$hkl$].

Figure 6 shows the average resultant cutting force of the eight directions as illustrated in Figure 2. Cutting forces measured along <100> directions were significantly larger than those of <110>, thus suggesting a greater ductility characteristics along <100>. Such phenomenal were expected due to a lower force required to cut the cleavage plane along <110> directions. A fourfold behaviour similar to the silicon cubic crystal structure, repeats at every 90°.
3.1.2 Analysis on Surface Roughness and Edge Chipping

Hitachi TM3030 SEM system and Zygo NewView 5020 optical surface profiler with 10x objective were used for both edge chipping and surface roughness analysis. Edge chipping was quantitatively characterized by measuring the length of edge chipping illustrated by the schematic shown in Figure 7. Assessment was performed in the middle region of each machined surface along [010] and [110] directions. Average measurement of the length of edge chipping was obtained from fifty equally spaced data points along an approximate length of 220 µm. Figure 8 and Table 5 shows the SEM images and quantitative measurements of the respective machined surfaces.

Results from both surface roughness and edge chipping measurement shown in Table 5 were in agreement with the orientation-dependent crack propagation behaviour of silicon. Relationship between fracture toughness of <110> and <100> shown in Table 2 suggests that \( K_{IC<100>} > K_{IC<110>} \). In addition, possible contribution of vibration from the cutting tool causes higher cutting forces to be measured along <100> directions. Vibration causes periodic variation in the actual depth of cut which leads to large ploughing effect on the cutting edge of work-piece and high frictional force. This leads to minor edge chipping and partial ductile quality on the bottom surface as shown in Figure 8a.

**Figure 6:** Results of average resultant cutting force at the respective cutting directions.
3.2 Effect of Different Cutting Tools during Silicon Micro-Milling

3.2.1 Analysis on Surface Roughness and Edge Chipping

Slot machining were performed along the [0\(\overline{1}0\)] direction using three different end-mills namely diamond coated tungsten carbide, CVD diamond and single crystal diamond tools. Figure 9 and 10 shows the SEM images of post machining quality by the three cutting tools and final machined slots on the (001) silicon wafer by each respective tools. From the SEM
images shown in Figure 9, single crystal diamond tool suffers the least amount of tool wear. In addition, both CVD and single crystal diamond tools achieved fracture free quality while diamond coated tungsten carbide tool suffers micro pits and cracks on the bottom machined surface as shown in Figure 10.

The bottom surface machined by CVD and single crystal diamond have no visible fracture because both tools having very sharp cutting edges due to the small cutting tool radius. This causes the impact force imposed on the sample surface during contact with the cutting tool to be small. Controversial results on the measured length of the edge chipping shown in Table 6 indicates that single crystal diamond tool generates the largest amount of edge chipping along the [010] cutting direction. Due to the limited understandings on tool wear in ultra-precision machining, no absolute explanation can be given to the mechanism of edge chipping behaviour when micro-milling was performed by either CVD or single crystal diamond tool. Diamond tool and diamond coated tool used in this experiment have different effective rake angle. The former has a nearly zero rake angle and the latter has a large negative rake angle.

Large negative rake angle is believed to create highly compressive hydrostatic pressure in the cutting zone, and the highly compressive hydrostatic pressure in turn can suppress the crack propagation. This might be a possible explanation to the large edge chip size generated during micro-milling with CVD or single crystal diamond tool. Although the method proposed to measure the length of edge chipping provides an estimation to quantify edge chipping, more precise method of quantification such as volume measurement of the generated chips should be investigated and applied in the future.
Figure 9: SEM images of tool wear on diamond coated, CVD and single crystal diamond tools.

Figure 10: SEM images of machined slot on (001) Silicon Wafer by diamond coated, CVD and single crystal diamond tools at $\approx 150 \, \mu m$ deep.

Table 6: Average and RMS Edge Chipping Length.

<table>
<thead>
<tr>
<th></th>
<th>Single Crystal Diamond Tool</th>
<th>CVD Diamond Tool</th>
<th>Diamond Coated Tungsten Carbide Tool</th>
</tr>
</thead>
<tbody>
<tr>
<td>Avg. Edge Chip Length (µm)</td>
<td>66.47</td>
<td>55.36</td>
<td>16.24</td>
</tr>
<tr>
<td>RMS Edge Chip Length (µm)</td>
<td>67.26</td>
<td>56.81</td>
<td>18.32</td>
</tr>
</tbody>
</table>
4 CONCLUSIONS
The effect of crystallographic orientation during micro-milling of silicon was investigated and assessed through its cutting performance in this study. Surface roughness and edge chipping measurement indicates that surface quality along <100> were better than those of <110>. Furthermore, cutting forces measured along <110> were lower due to lower fracture toughness and surface energies. This leads to easy dislocation of atoms along <110> directions. Results from the comparative studies of different end-mills shows that diamond coated tool generates the least edge chipping, while single crystal diamond and CVD tool both generates fracture free bottom surface. Tremendous tool wear on diamond coated and CVD tools remains a challenge after silicon micro-milling operations.

This research is in agreement with previous studies as a significant presence of crystallographic orientation effect affecting the quality of machined silicon by micro-milling. Furthermore, results from this research had demonstrated that by machining silicon along <100> directions, partial fracture free machining quality were achieved. However, in conjunction with previous studies, it was recognized that fracture free machining quality for brittle materials cannot solely depend on mechanical micro-machining. Generation of edge chipping remains a challenge even if machining were conducted along <100> directions. Future perspectives to develop mitigating strategies for such challenge would include the introduction of protection techniques to the silicon to minimise edge chipping.

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